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Monolayer Superconductivity in WS₂

Oleksandr Zheliuk, Jianming Lu, Jie Yang, and Jianting Ye*

Superconductivity in monolayer tungsten disulfide (2H-WS₂) is achieved by strong electrostatic electron doping of an electric double-layer transistor (EDLT). Single crystals of WS₂ are grown by a scalable method – chemical vapor deposition (CVD) on standard Si/SiO₂ substrate. The monolayers are identified by both AFM and color-coding techniques. The EDLT device based on single-layer WS₂ shows ambipolar transfer characteristics indicating a semiconducting nature of the material. Metallic transport on the electron side evolves into superconductivity with critical temperature $T_c = 3.15$ K.

Semiconducting transition-metal dichalcogenides (TMD) such as MoS₂, MoSe₂, WS₂, and WSe₂ are known candidate for two-dimensional (2D) electronics due to their layered composition and unique band structure, which undergoes indirect to direct band gap transition when exfoliated down to the monolayer. The absence of inversion symmetry in monolayer hexagonal lattice together with strong spin-orbit interaction coming from heavy transition metal ion leads to a lifted spin degeneracy near the band edges, making this material a testing platform of various phenomena of solid state physics.^[1,2]

Superconductivity is known to exist in alkali metal intercalated bulk MoS₂ with T_c up to 7.3 K.^[3] Recently discovered electrostatically induced superconductivity in multilayer MoS₂ flakes paves a way to more controllable superconducting devices, where T_c can be electrically tuned from 0 to 10 K.^[4] A similar technique has been used to induce superconducting state on the surface of bulk WS₂ with $T_c \sim 2$ K at half of normal resistance $R_N^{50\%}$.^[5] Such a state is believed to originate from the top most layer and show the properties of true 2D superconductor such as BKT transition, coherence length larger than superconductor thickness, the high anisotropy of upper critical field with pronounced cusp shape. Among other unique properties, primarily, electrons of spin-polarized Cooper pairs residing at K and K' valleys makes TMDs highly resilient against applied in-plane magnetic field.^[6]

In most transport studies of TMDs on ultrathin flakes, samples were prepared by mechanical exfoliation from bulk. Another approach is to use chemical vapor deposition (CVD) growth to obtain ultrathin flakes down to single layer. Widely developed for TMD materials, the CVD technique allows obtaining monolayer single-crystal domains with a lateral size

of ~ 20 – 100 μm , which can be easily accessed by standard microfabrication processes. However, the quality of crystalline monolayer prepared by CVD is usually expected to be lower than the mechanically exfoliated ones making CVD samples less studied for their basic physical properties.

Typical multilayer WS₂ grown by CVD method is shown in the inset of **Figure 1**. Steps corresponding to different layer number can be clearly resolved in the AFM profile scan. Here, the total thickness of the flake is $d = a + (N - 1) \times b$, where N – is

the layer number, a – is the thickness of the bottom layer together with Van der Waals gap against the substrate and it ranges from 0.8 to 1 nm, while b – is the thickness of each layer ~ 0.6 nm – consistent with previous reports,^[7] the number of layers can be determined consistently by color coding technique, which is alternative technique suitable for ultrathin layered 2D materials on flat substrates such as Si/SiO₂.

The histogram represents sample area (in pixel counts) as a function of reflected intensity on the green channel of CCD. Peak with the highest area as well as highest reflected intensity corresponds to a bare Si/SiO₂ substrate reflection. Next peak at 158 counts corresponds to the reflection from the monolayer, reflection from each next layer reduces total intensity by 7.4%. The normalized intensity of reflection on the green channel as a function of layer number shows linear decrease up to five layers (see right inset of **Figure 1**). It shows saturating dependence when the number of layers increases further.

Large area single layer WS₂ flakes were selected for the fabrication of electric double-layer transistor (EDLT) devices. Standard e-beam lithography defines electrodes composed of 5/50 nm of Ti/Au. Before measurement, the sample was covered with a small droplet of ionic liquid: DEME-TFSI. The transfer curve measured at 220 K is shown in **Figure 2**. Both valence and conduction band transport can be accessed within gate bias $V_G = \pm 4$ V range with on current reaching 10 and 6 μA for accumulating holes and electrons, respectively. Hysteresis in the order of 1.5 V observed during measurement is caused by reduced ion mobility at 220 K. Insulating regime spans around 2 V consistent with a direct band gap of monolayer WS₂. At $V_G = 0$ V devices are usually insulating indicating high crystallinity and absence of charged impurities. Repeatability was found for most of the devices indicating chemical stability within the electrochemical window of ± 4 V.

Keeping the gate voltage constant, the sample was cooled below glass transition temperature of the ionic liquid (~ 190 K) with 3 K min^{−1} to ensure the fast freezing of ion movement. When temperature reached below 170 K gate voltage was set to $V_G = 0$ without losing the induced electronic state.

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Figure 1. Layer number identification of CVD-grown WS₂ by color coding technique. Sample area plotted as a function of reflected intensity (represented by pixel count and green channel counts, respectively). Peak with the largest number of counts corresponds to the reflection from the bare substrate. Top left inset: optical image of multilayer WS₂ flake and AFM profile measured across the sample. Top right inset: number of green channel counts normalized with respect to bare substrate counts plotted against layer number.

The resistances of probes 3-4 and 7-8 measured as a function of temperature are shown in **Figure 3**. Overall sample shows metallic behavior down to low temperature, where normal resistance reaches 1.4 and 1.8 k Ω for a different channel. Both channels undergo superconducting transition below 4 K, which can be clearly resolved when the excitation current is reduced from 6.8 to 1.2 μ A. (Figure 3). Slight difference in T_C is attributed to the spatial variation of doping level across the sample surface. It is interesting to note that $T_C = 3.15$ K is higher than achieved by a similar method in bulk WS₂ crystals.^[5]

Large magnetoresistance below transition temperature confirms the presence of the superconducting state in monolayer

Figure 3. Resistance of monolayer WS₂ as a function of temperature close to superconducting transition. Contact leads 3-4 and 7-8 are probing different sample region (numbers correspond to those in **Figure 2**). Upper and lower set of data correspond to high (6.8 μ A) and low (1.2 μ A) excitation current. Inset: resistance measured up to high temperatures.

WS₂. As shown in **Figure 4**, the perpendicular critical field $B_{C2}^{\perp}$ increases linearly with the lowering of sample temperature. The $B_{C2}^{\perp}$ obtained by $R_N^{50\%}$ criteria reaches ~ 1.05 T by extrapolation to zero temperature similar to that found in monolayer MoS₂.^[8] From the linear dependence of $B_{C2}^{\perp}$ which is consistent with the standard linearized Ginzburg–Landau theory, we estimate the superconducting coherence length $\xi_{GL} = 17.7$ nm which is much larger than the 0.6 nm thick monolayer and confirms formation of a 2D superconducting state in monolayer WS₂.

In conclusion, here we reported the first observation of superconducting state in CVD grown WS₂ monolayer crystal, which is industrially friendly and air stable semiconducting

Figure 2. Transfer characteristics of EDLT device based on monolayer WS₂. Arrows indicate the direction of gate voltage sweep. Inset: optical image of the typical device structure.

Figure 4. Resistance of WS₂ superconductor under perpendicular magnetic field from -3 to 3 T. Each curve corresponds to the sample at different temperatures ranging from 2.1 to 2.8 K. Inset: critical field as a function of temperature.

material. Surprisingly, critical temperature $T_C = 3.15$ K is higher than in its bulk counterpart indicating CVD-grown monolayer shows at least similar sample quality as compared to the samples prepared by mechanical exfoliation.

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Conflict of Interest

The authors declare no conflict of interest.

Keywords

chemical vapor deposition, monolayers, superconductivity, transition metal dichalcogenides

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