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Enhanced conductance in Superconductor Semiconductor junctions at zero voltage.

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An enhancement of the zero-voltage conductance of a niobium-silicon junction is found at very low temperatures. This enhancement is a result of quantum interference of electrons, near the superconductor semiconductor interface, which results in an increase of Andreev reflection probability.

1. INTRODUCTION

At the interface between a normal metal (N) and a superconductor (S), a process called Andreev reflection [1] can occur: an incoming electron, with an energy below the superconducting gap, is reflected as a hole. This process will double the current, and therefore double the conductance. If a barrier exists at the S-N interface, the Andreev reflection probability will be smaller than 1, and in general energy dependent [2]. Experiments on Nb-InGaAs contacts [3] have shown a conductance enhancement at low voltages, which can not be explained by the simple normal metal-superconductor model introduced by Blonder et al [2]. In refs. [4-7] the suggestion was made that impurity scattering could lead to enhancement of the Andreev reflection probability, which can account for an enhanced conductance.

2. EXPERIMENTS

To be able to study a single SN interface, we followed two different approaches. We used degenerately doped Si membranes \(8 \cdot 10^{18} \text{cm}^{-3} \text{B} \) of either 50 nm or 500 nm thickness, which were made as described in ref. [8]. It was shown previously that for the 50 nm thin membranes, the thickness of the Si layer is small compared to \(l_0\). Fig. 1 gives a schematic view of the samples. In the 500 nm membrane sample, the two Nb electrodes are so far apart (with respect to \(l_0\)), that the junction essentially consists of two decoupled S-N interfaces in series. The second system is a double barrier SININ' structure with a 50 nm Si layer. In these samples we have a Nb contact on top, and a normal metal, either W or Au, as backside electrode. Although in this system the two barriers are not decoupled, because of the small Si layer thickness, we are able to study a single SN interface in this manner. Fig. 2 shows the differential conductance of the Nb-50 nm Si-W junction, for various temperatures.

A clear zero-voltage conductance enhancement, with respect to the conductance at higher temperatures, is observed, which drops rapidly with increasing voltage. The conductance peak has completely disappeared at 1.2 K. For lower temperatures it saturates below 100 mK. These observations are in agreement with earlier measure-
ments on the Nb-500 nm Si-Nb junctions. Magnetic field dependent measurements are shown in Fig. 3. The influence of the magnetic field on the zero-voltage conductance is qualitatively the same as that of temperature. The conductance enhancement is suppressed completely at a field of approximately 100 mT, which is in good agreement with predicted values of the critical field [7]. In the Nb-Si-Au junctions, where the normal metal contact resistance is smaller than the Nb-Si contact resistance (in the Nb-Si-W junctions the normal metal contact resistance dominant), we find an even greater enhancement of the zero-voltage conductance. Despite the low transparency of the S-N interface (of the order of 0.1), the zero-voltage conductance in those samples is higher than the normal state conductance. Similar conductance curves were measured recently by Heslinga et al [9], who used point contact spectroscopy on a Si membrane with a Nb backside contact.

3. CONCLUSIONS

Our measurements show a clear enhancement of the zero-voltage conductance at low temperatures and zero magnetic field. Further analysis of the experimental observations requires the application of the ideas of ref. [4-7] to the specific geometry of our samples. Especially with respect to impurity scattering and scattering at the barriers.

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